

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10540606
<b>Filing Date:</b>	27-Jun-2005
<b>Title of Invention:</b>	Method and apparatus for manufacturing circuit board
<b>First Named Inventor/Applicant Name:</b>	Toshiaki Takenaka
<b>Filer:</b>	Lawrence E. Ashery
<b>Attorney Docket Number:</b>	MAT-8716US

Filed as Large Entity

### **U.S. National Stage under 35 USC 371 Filing Fees**

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
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**Basic Filing:**

**Pages:**

**Claims:**

**Miscellaneous-Filing:**

**Petition:**

**Patent-Appeals-and-Interference:**

Notice of appeal	1401	1	540	540
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**Post-Allowance-and-Post-Issuance:**

<b>Extension-of-Time:</b>				
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Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
<b>Total in USD (\$)</b>				<b>540</b>